
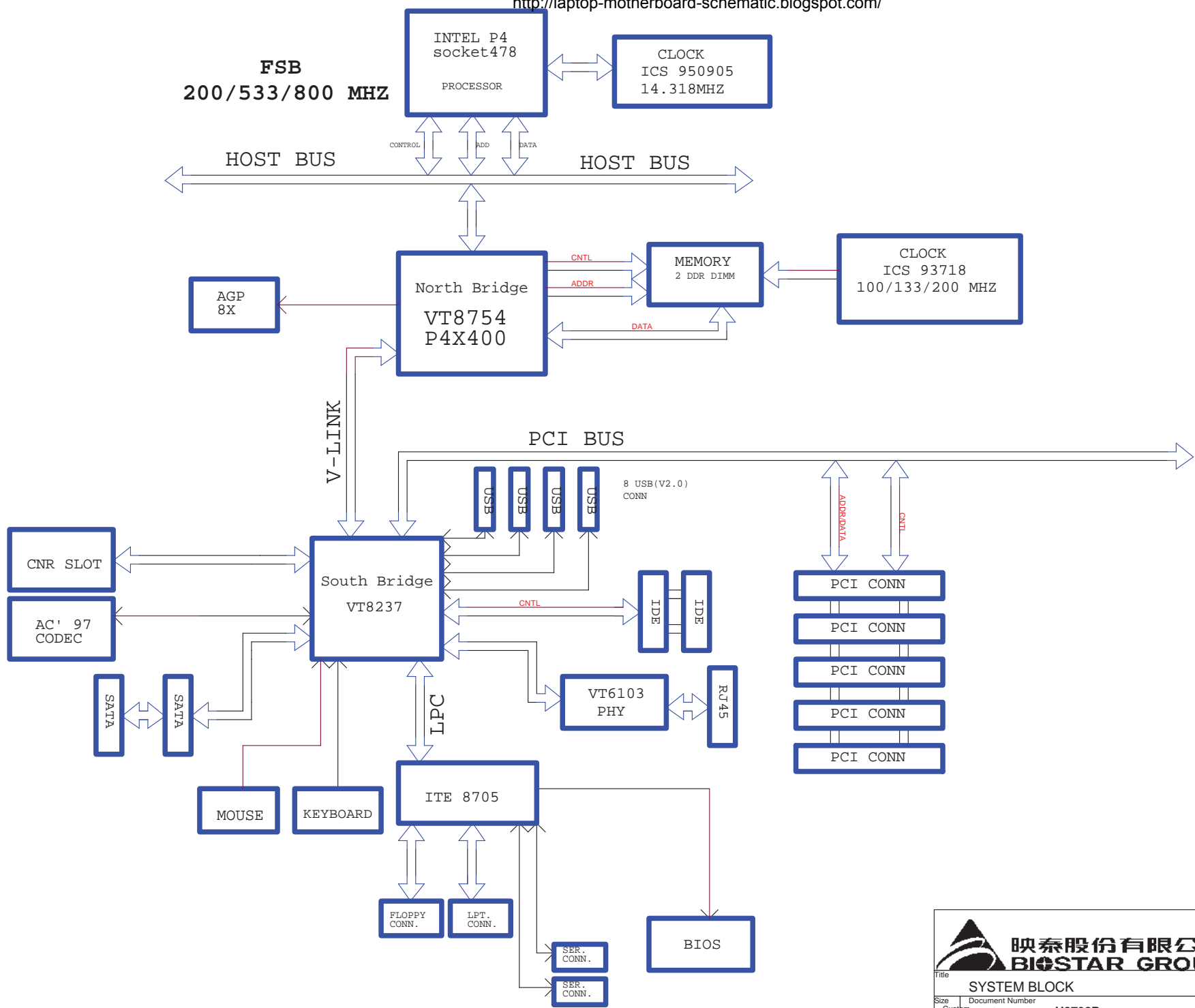



**BIOSTAR GROUP****VIA P4X400 + VIA VT8235CD Chips****U8798P****(U8798 Pro)****VER:1.3**

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Title SYSTEM BLOCK		
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## M7VIP Grand

1. CPU --- AMD Socket-A(2-Phase Power)
2. CHIPSET --- VIA KT333CF + VT8237
3. MEMORY --- 3 DDR SDRAM(Max. 3GB)
4. SLOTS --- AGP(4X)x1, PCIx5, CNRx1
5. CODEC --- C-Media CMI9739A 6-Channel Audio
6. LAN PHY --- VIA VT6103
7. SATA --- Marvell 88I8030
8. PCB Size = 30.5cmX20.5cm, 4-Layer



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GENERAL SPEC				
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Change list:

Initlization M7VXK 0.90 sch

0.90 -> 0.91

- 1.Add a 3904 and 200 ohm in below circuit,that is a patch circuit for PERR function of the AGP 8X/4X card.
- 2.Del ACR slot
- 3.Add Promise serial ATA function on borad
- 4.Change FM3570 to ATXP3 (support over voltage)
- 5.Del card reader

0.91 -> 0.92

- 1.Solve S3 resume back fail
- 2.Solve me,2000 excute 3dmark2001se will hang up problem
- 3.Solve audio can't disable by BIOS
- 4.Change game port usb common choke for EMI
- 5.Change freq. default to over clocking
- 6.Del IEE1394
- 7.Add CMI8738 H/W sound function
- 8.Change Realtek lan PHY to VIA LAN PHY

0.91 -> 0.93

- 1.Solve S3 resume back fail
- 2.Solve me,2000 excute 3dmark2001se will hang up problem
- 3.Solve audio can't disable by BIOS
- 4.Change game port usb common choke for EMI
- 5.Change freq. default to over clocking
- 6.Solve MSI geforce 4 MX420 can't display problem
- 7.Solve SIS AGP 8X card cna't display problem

Change from M7VXK 0.93 to M7VXT 0.90 sch

0.90 -> 0.91

- 1.Solve S3 resume back fail
- 2.Del USB common choke
- 3.Solve power LED can not turn on when use OLED problem
- 4.Change V-term form 33ohm to 47 ohm to solve some DDR module execute will be fail problem
- 5.Solve clock buffer SM bus will be effect when problemming mail clock gen.
- 6.Solve sound codec will hane noise when boot , enter OS and shutdown power

0.91 -> 0.92


- 1.Solve 100mhz cpu S3 resume back fail
- 2.Add case open function jumper
- 3.Add front audio connector 6 channel function
- 4.Add SPDIF connector function

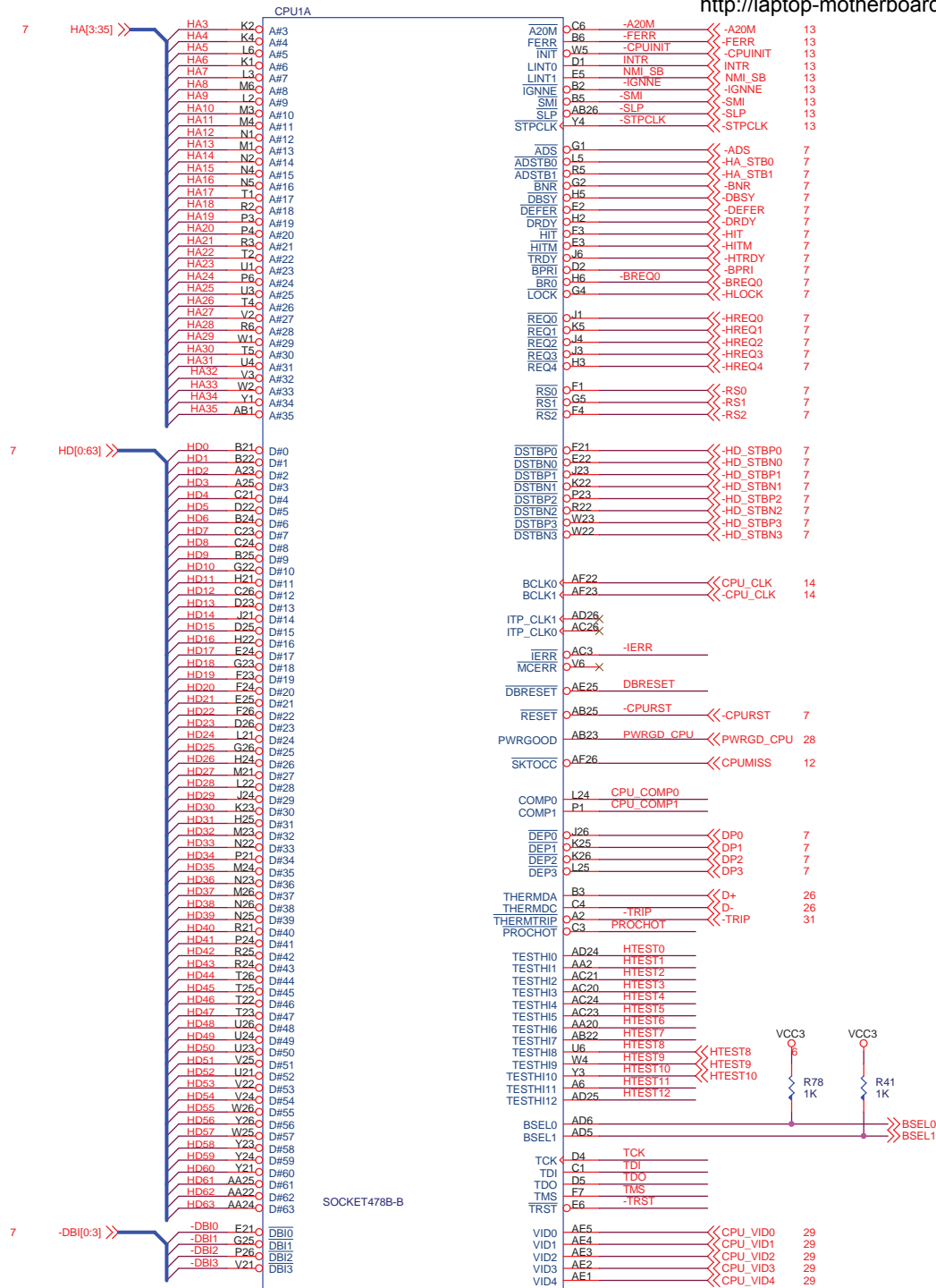
0.92 -> 1.0

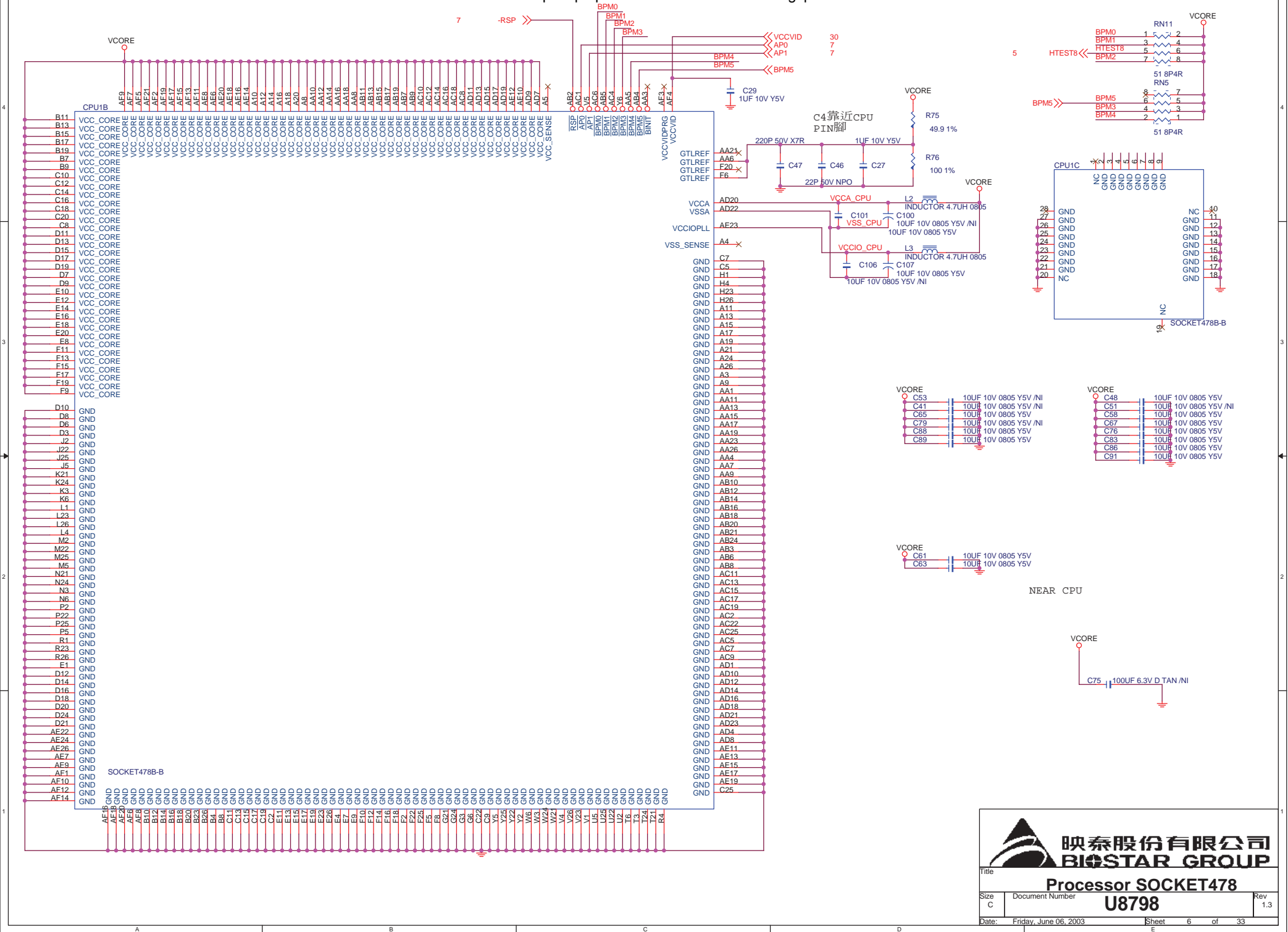
- 1.change version form 0.92 to 1.0

1.0 -> 1.1

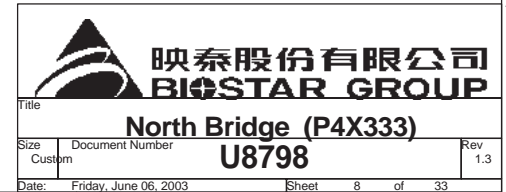
- 1.Modify case open intruder circuit
- 2.Add lan function
- 3.main clock and clock buffer combo to single chip
- 4.Del dimm3 socket
- 5.Modify SPDIF OUT curcuit for new cable
- 5.Modify audio codec curcuit to support C-media req.

			
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Title			
Change Lise			
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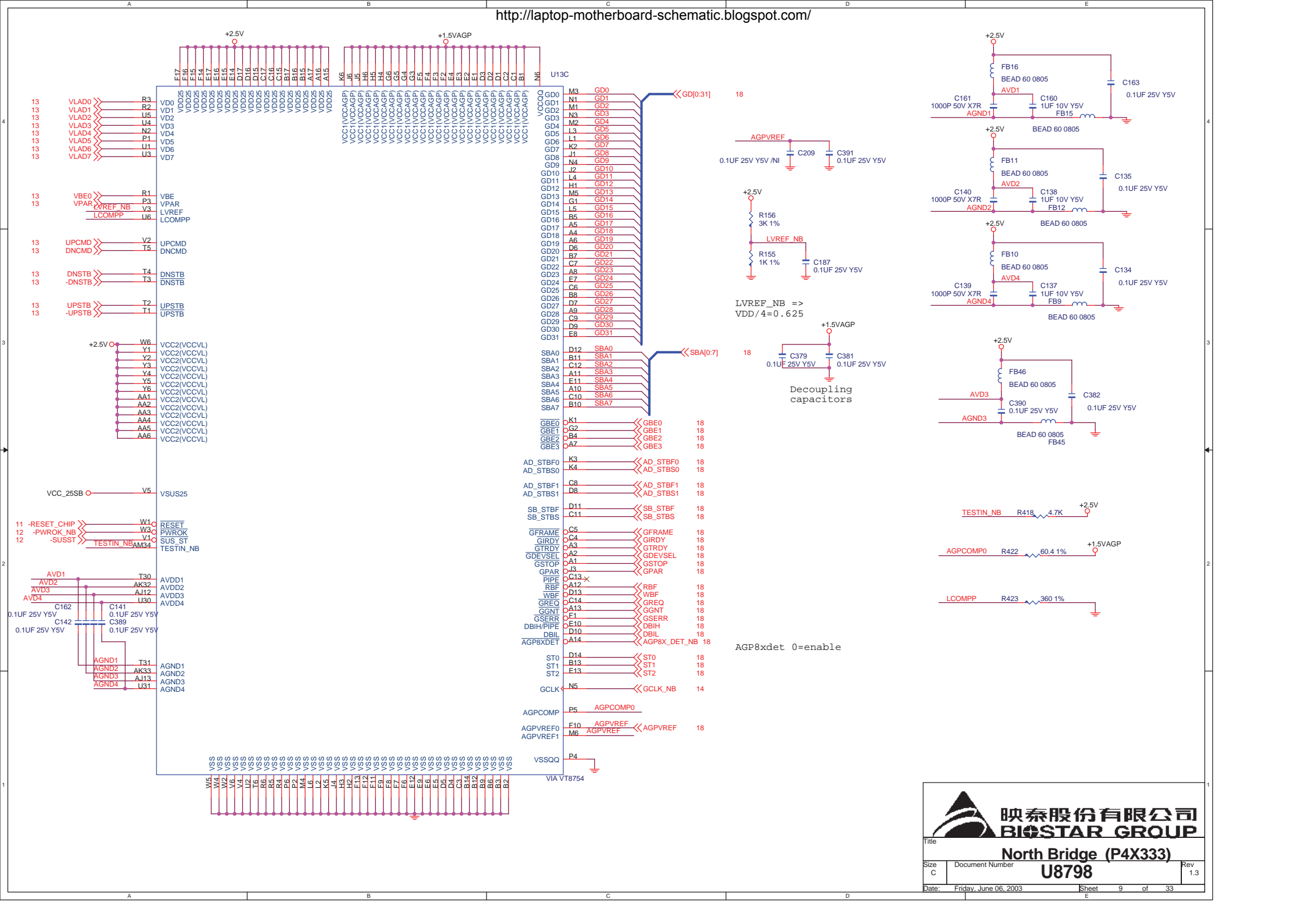
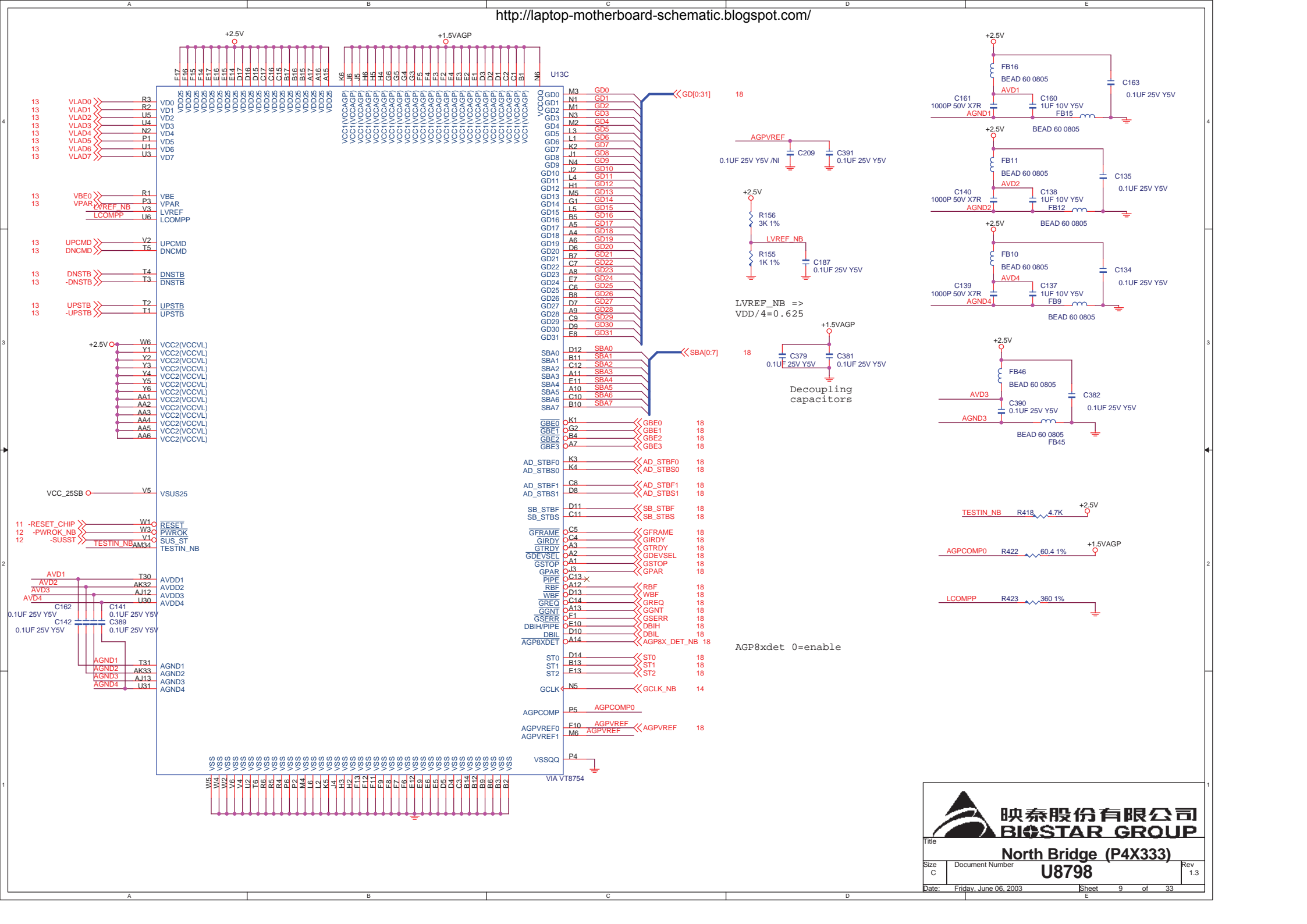
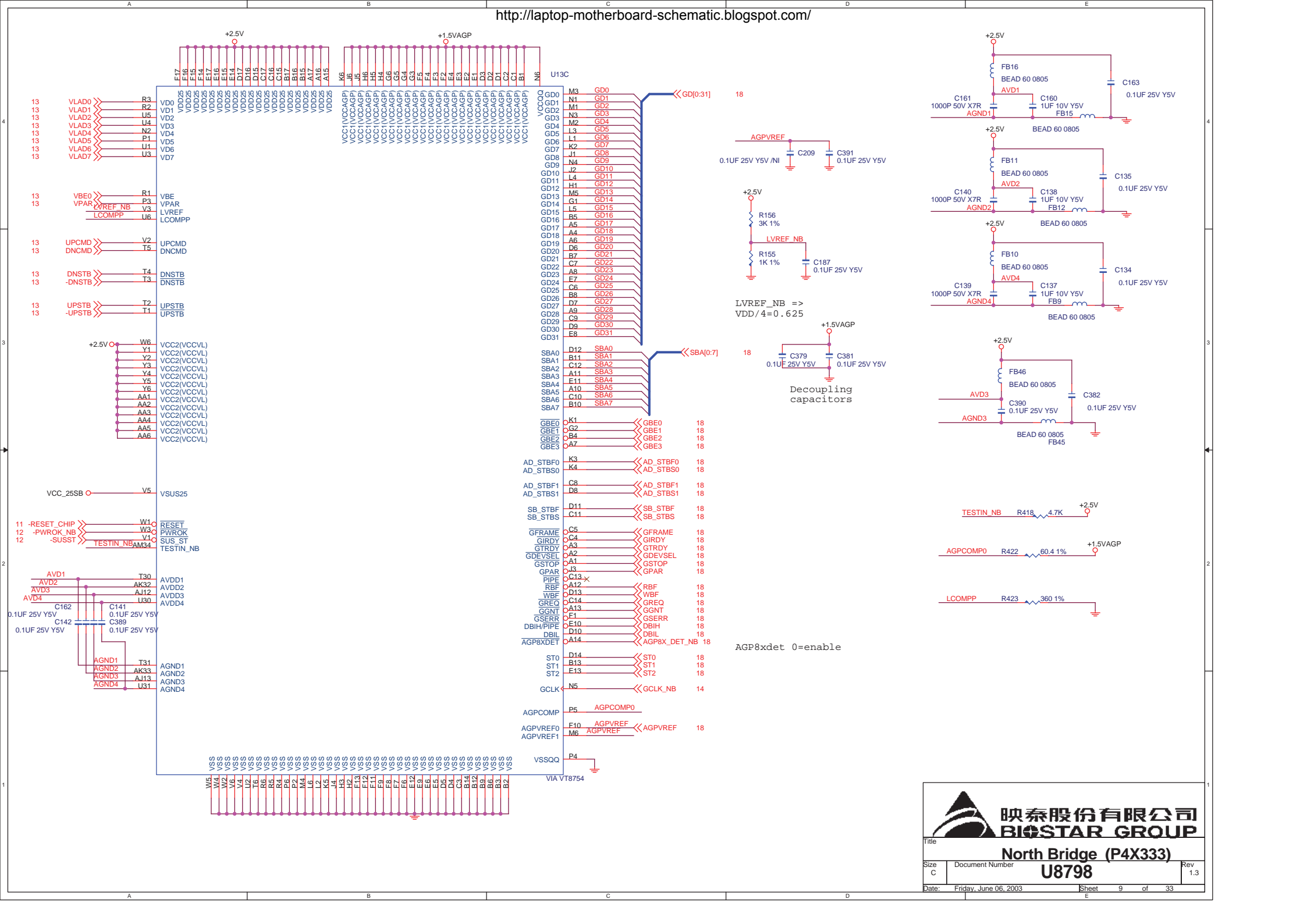
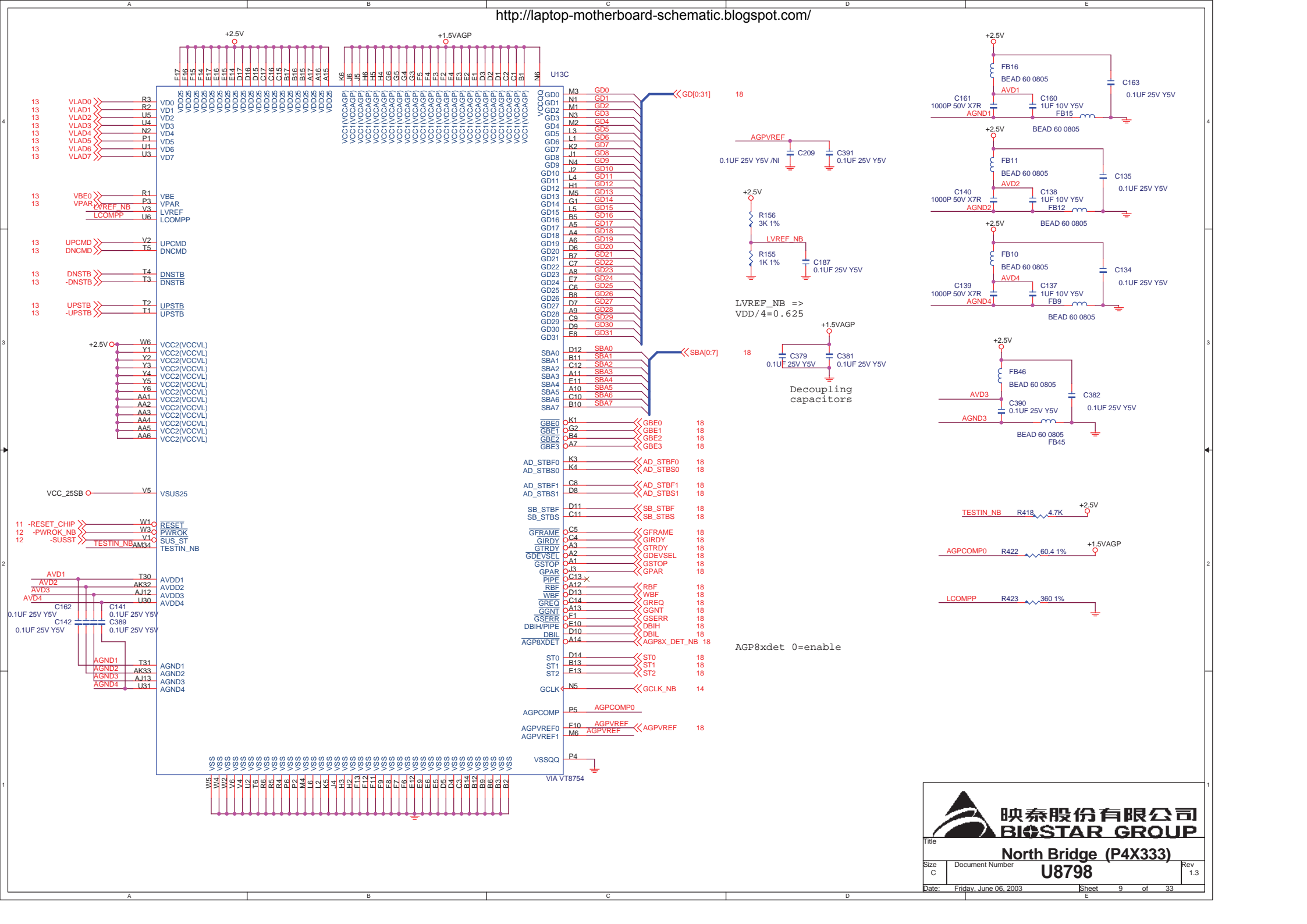


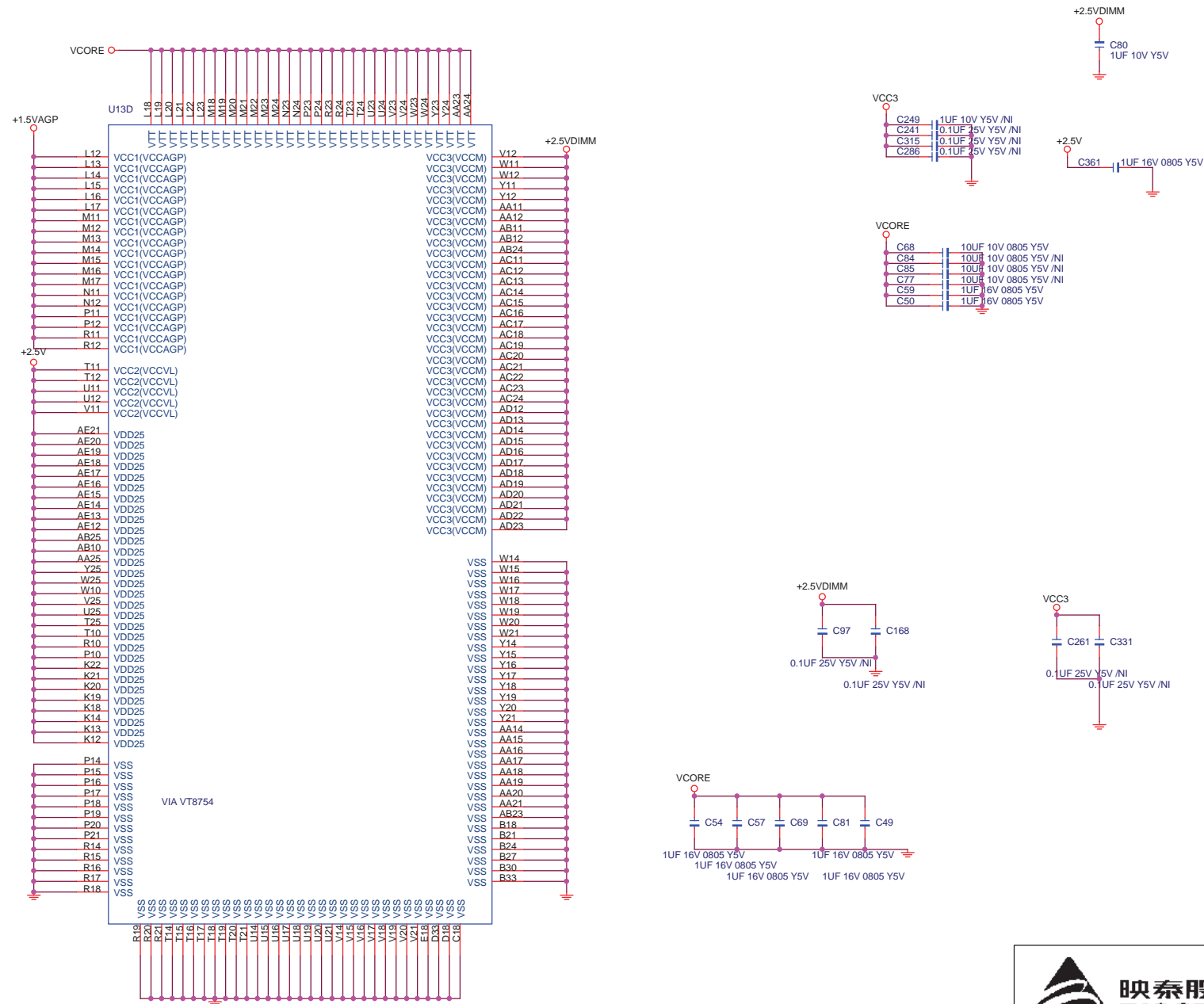


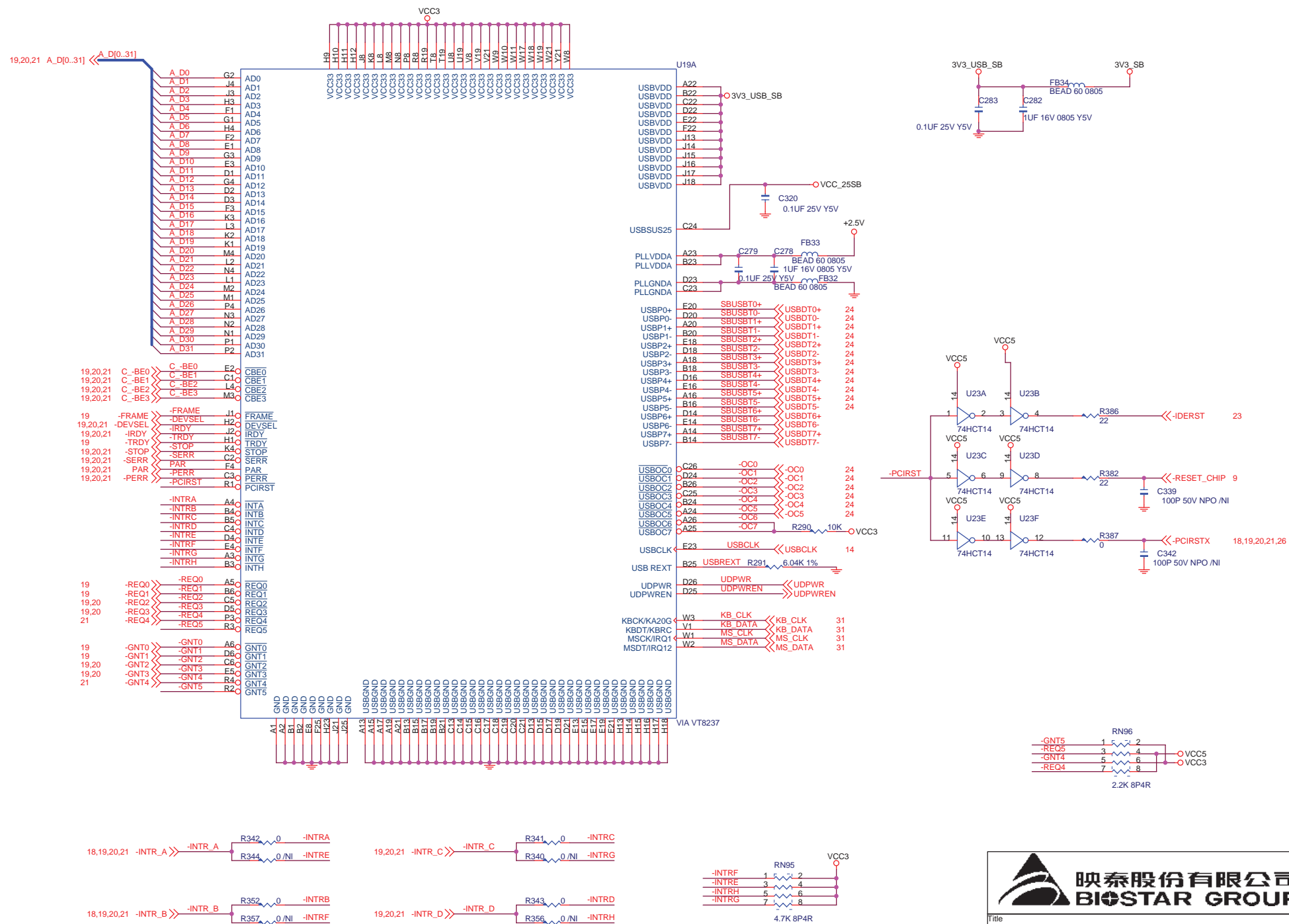




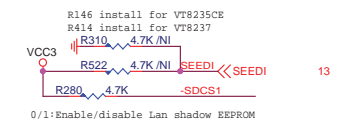








GPIOD	GTL pullup
0	Enable
1	Disable

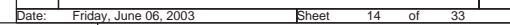
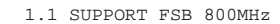


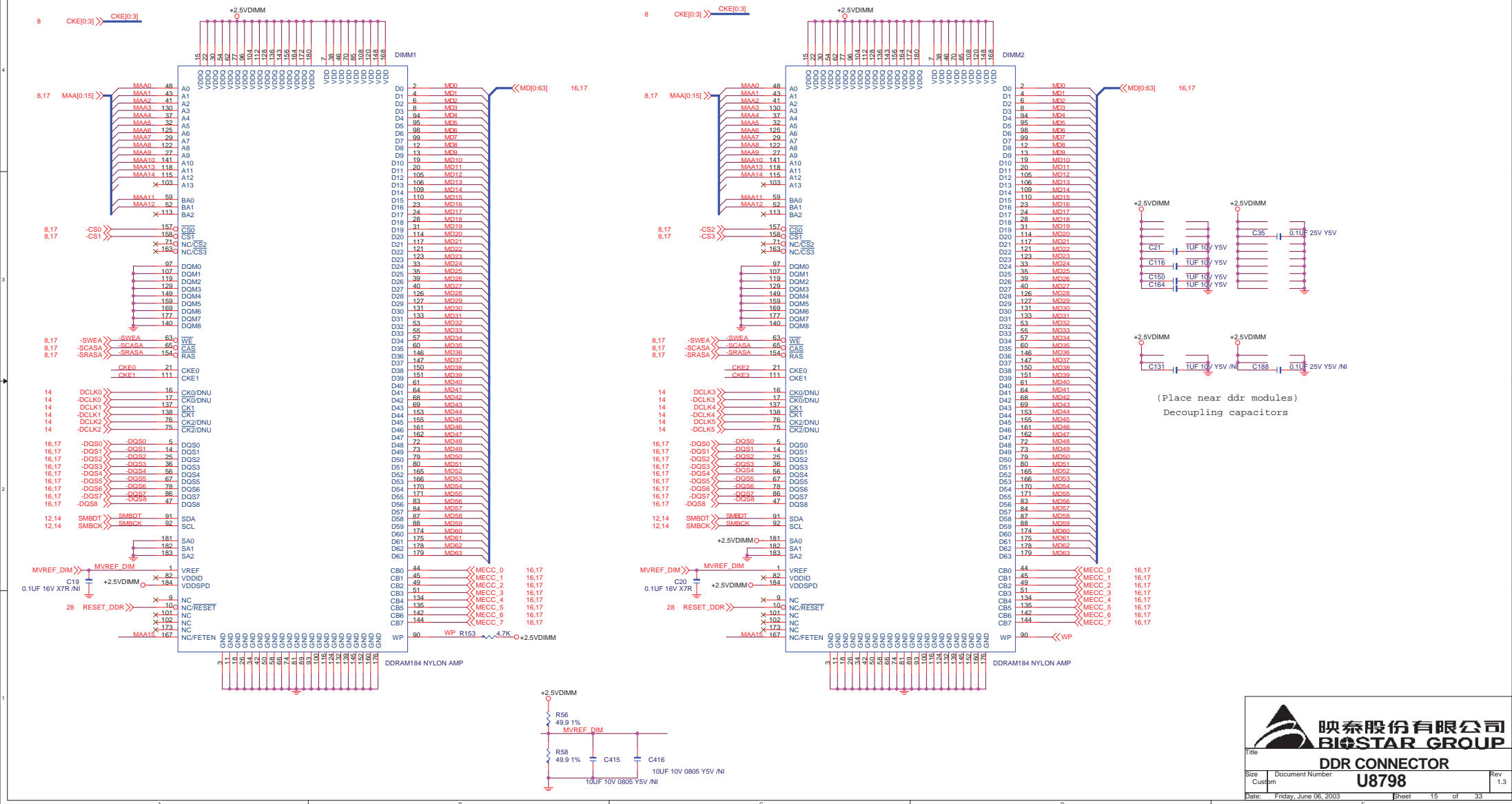
SOUTH BRIDGE (PM HW/ISA)

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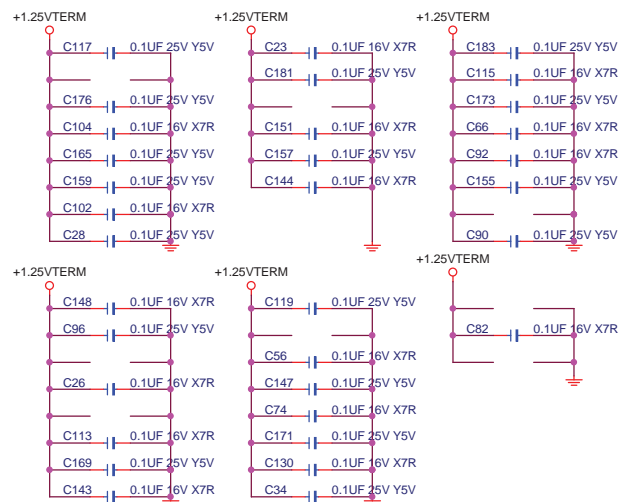
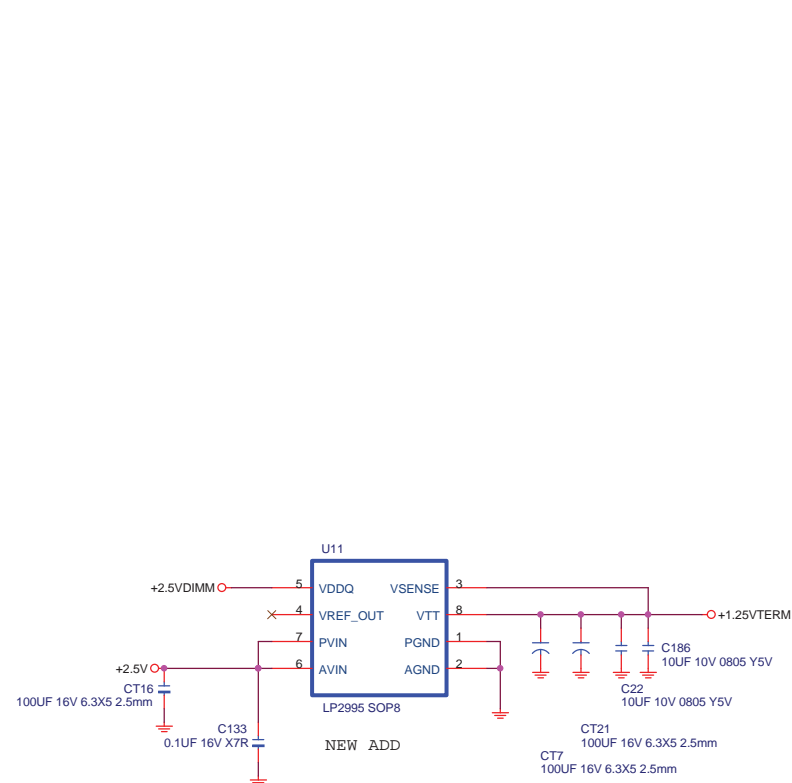










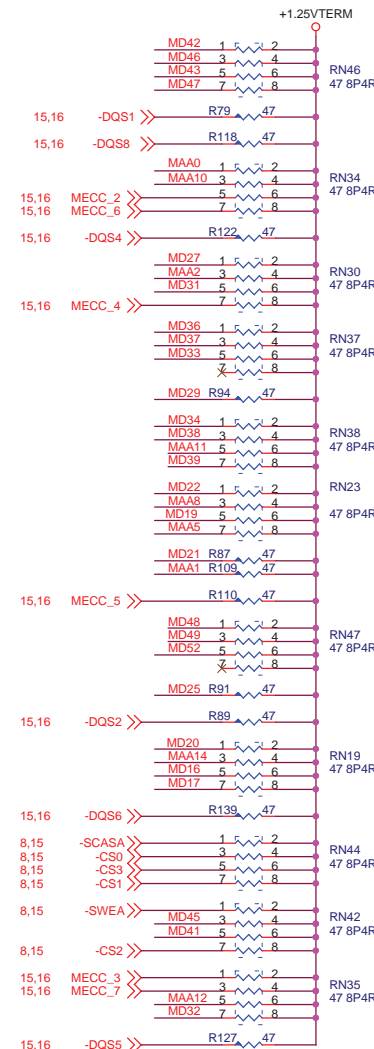
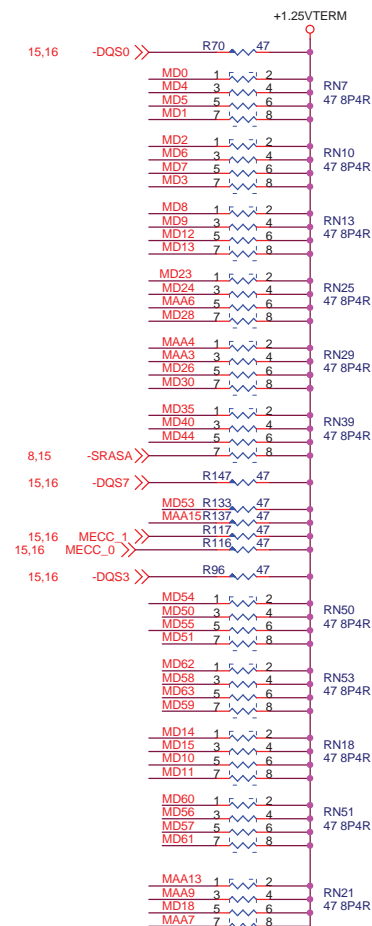


15,16 MD[0:63] >> MD[0:63]

8,15 MAA[0:15] >> MAA[0:15]

MAB[0:15] >> MAB[0:15]

8,15 CKE[0:3] >> CKE[0:3]

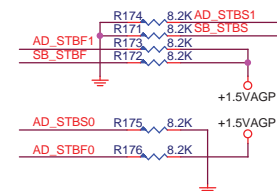


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**BIOSTAR GROUP**

Title: **DDR TERMINATION**

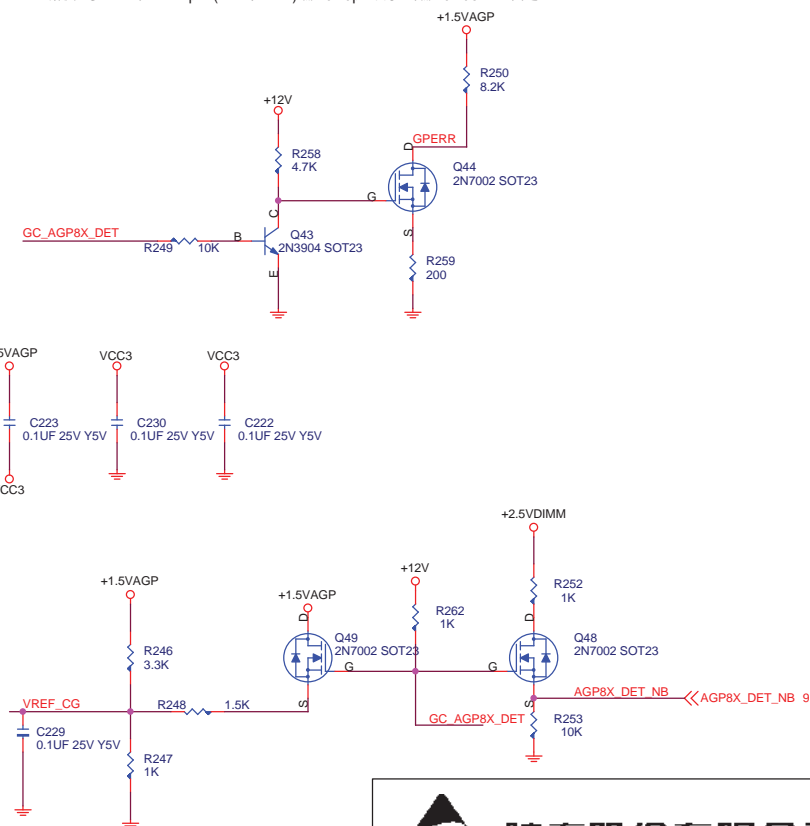
Size: Custom Document Number: **U8798** Rev: 1.3

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0.92 del RN72,RN74,RN75,RN40(8.2K 8P4R),  
R358,R294(8.2K),Q62,Q63(NDS351N),R313(0 ohm) VIACHIPSET 內部已處理

解決AGP 4X時，A12 pin (PIPE/DBIH) 需Pull up，而8X時需Pull down之問題



Decoupling capacitors  
(Place near AGP slot)

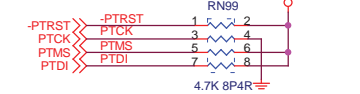
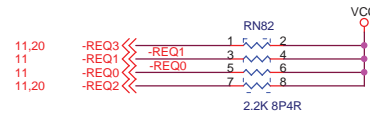
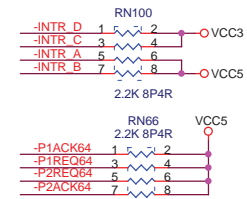
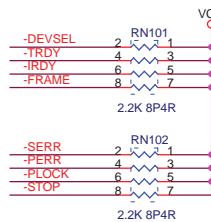
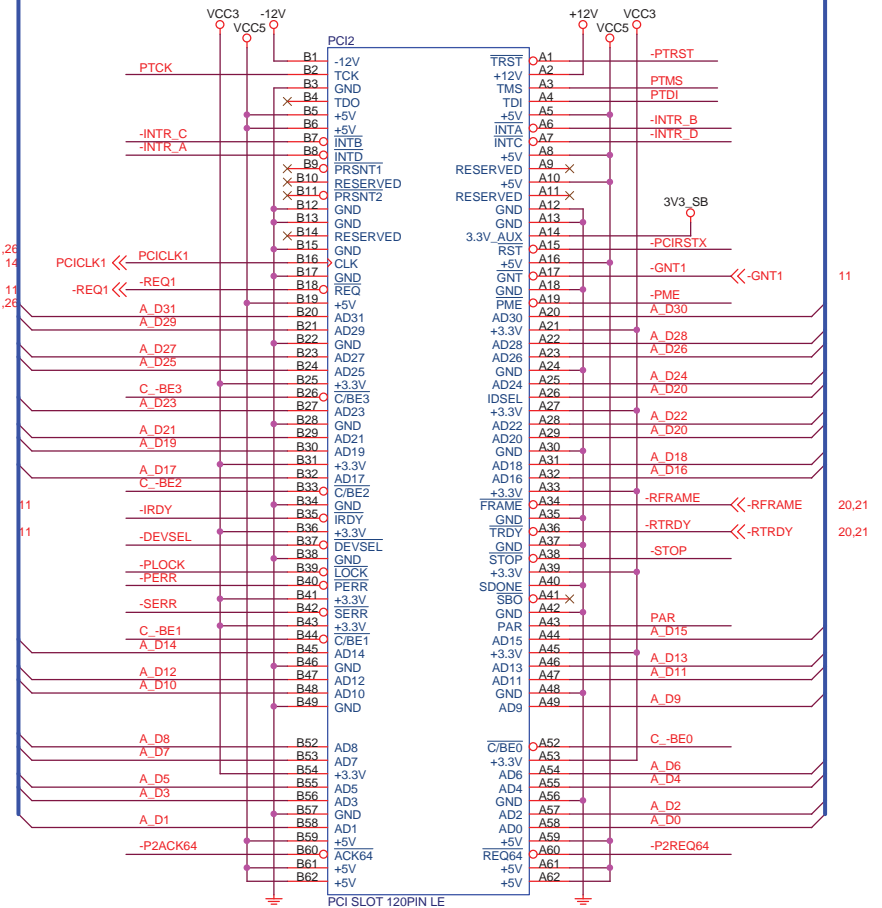
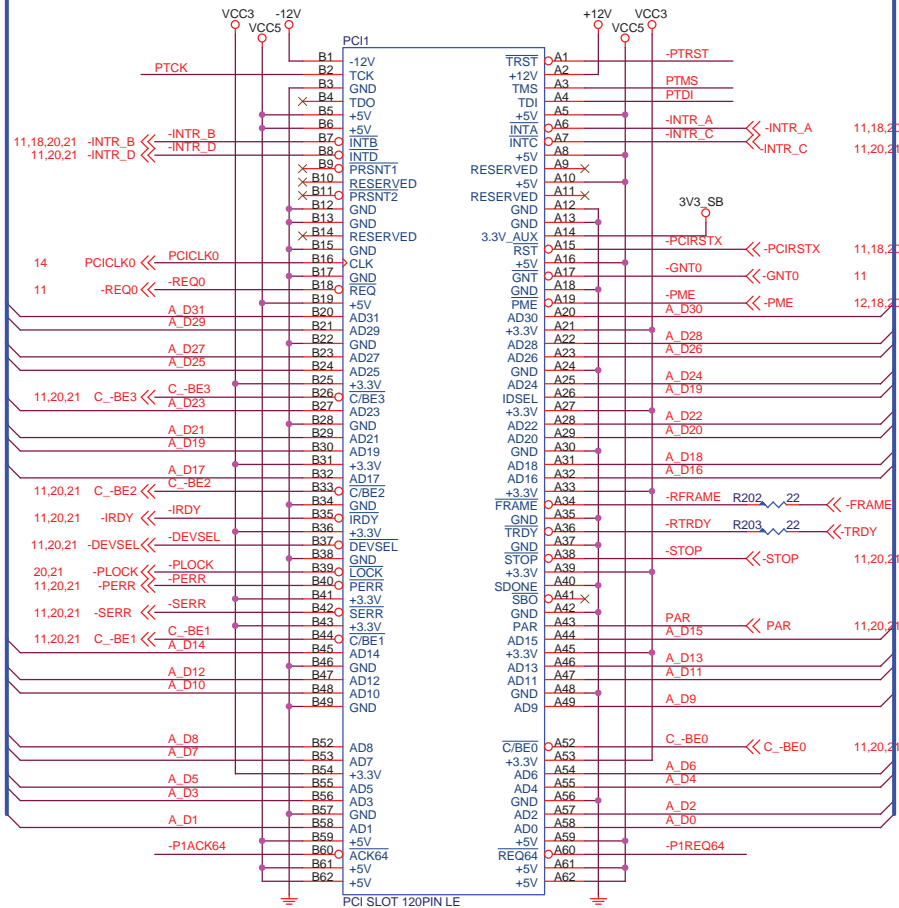
**Vref:**  
 2x : 0.4VDDQ=600mV  
 4x : 0.5VDDQ=750mV  
 8x : 0.23VDDQ=345mV



11,18,20,21 A\_D[0:31] <<

PCICLK0  
-INTR\_A  
-REQ0 / -GNT0  
A\_D19

PCICLK5  
-INTR\_B  
-REQ1 / -GNT1  
A\_D20



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**BIOSSTAR GROUP**

Title: PCI1 & PCI2 SLOTS

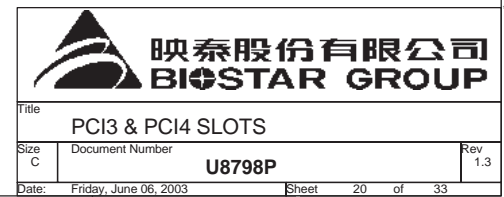
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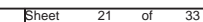
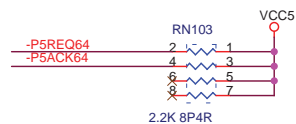
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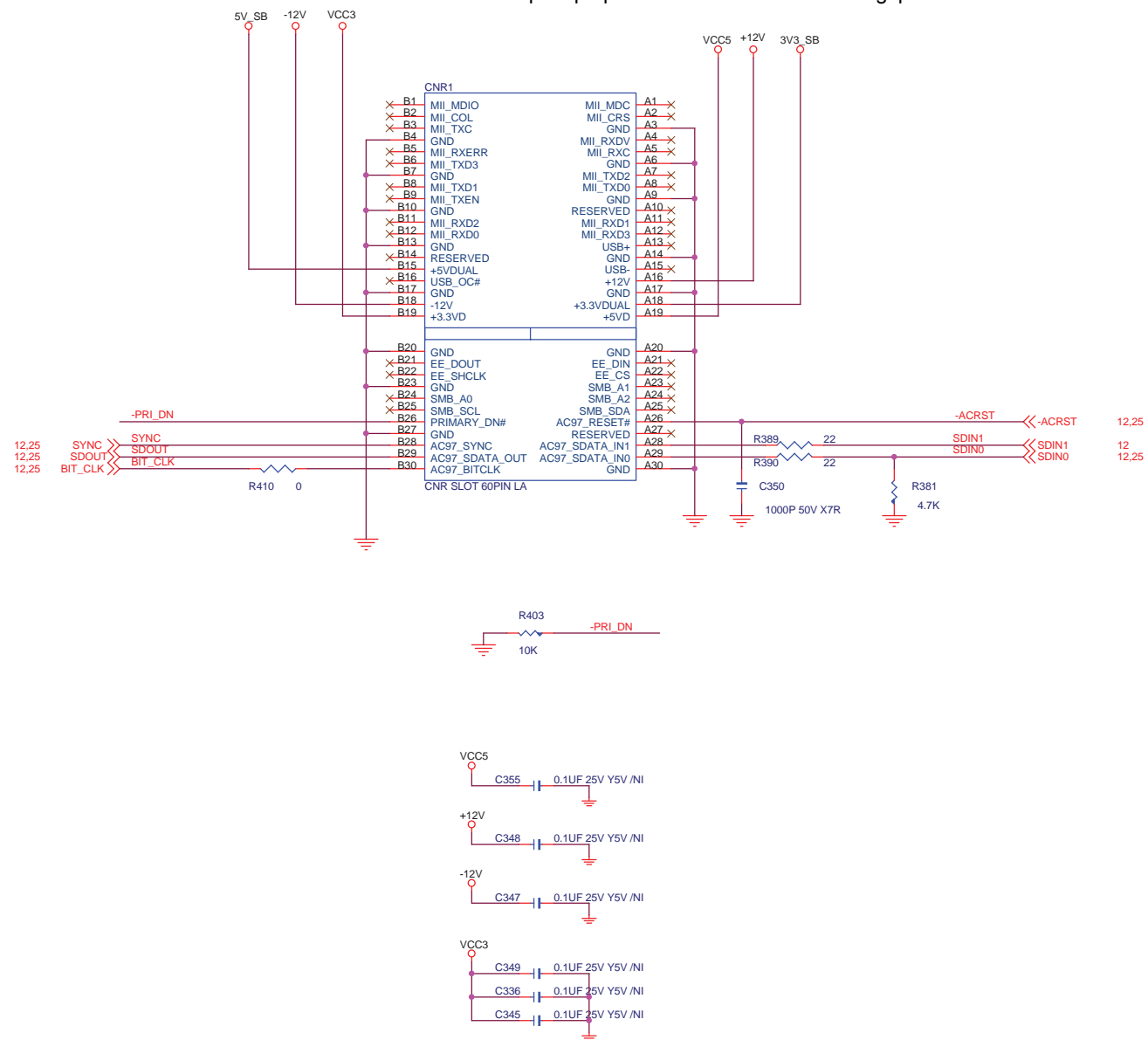
```

-INTR_D
-REQ3 / -GNT3
A_D22

```



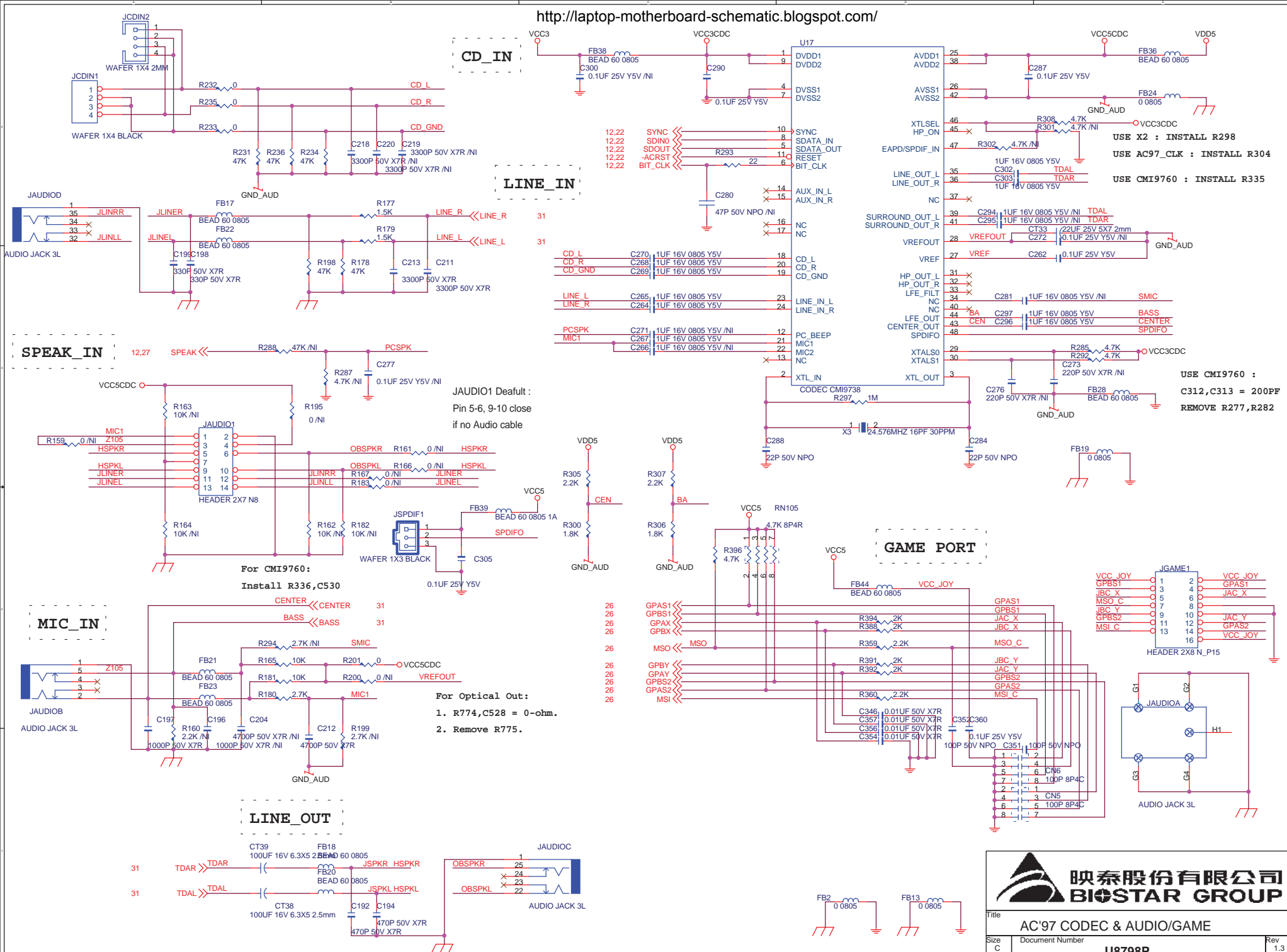








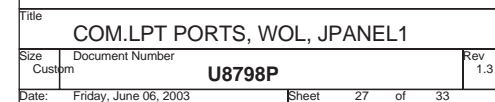
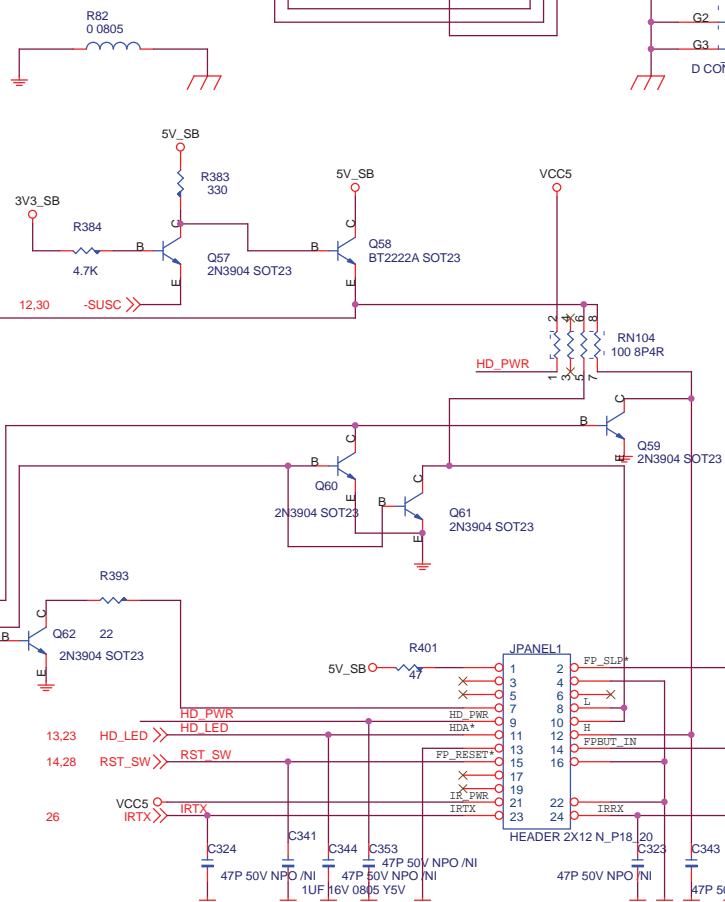


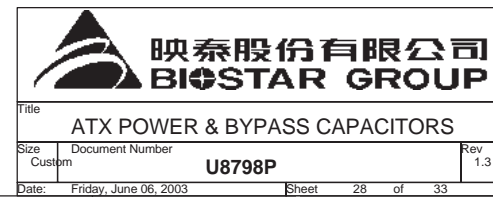


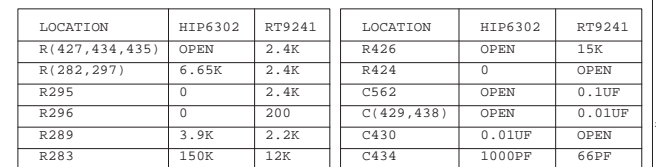


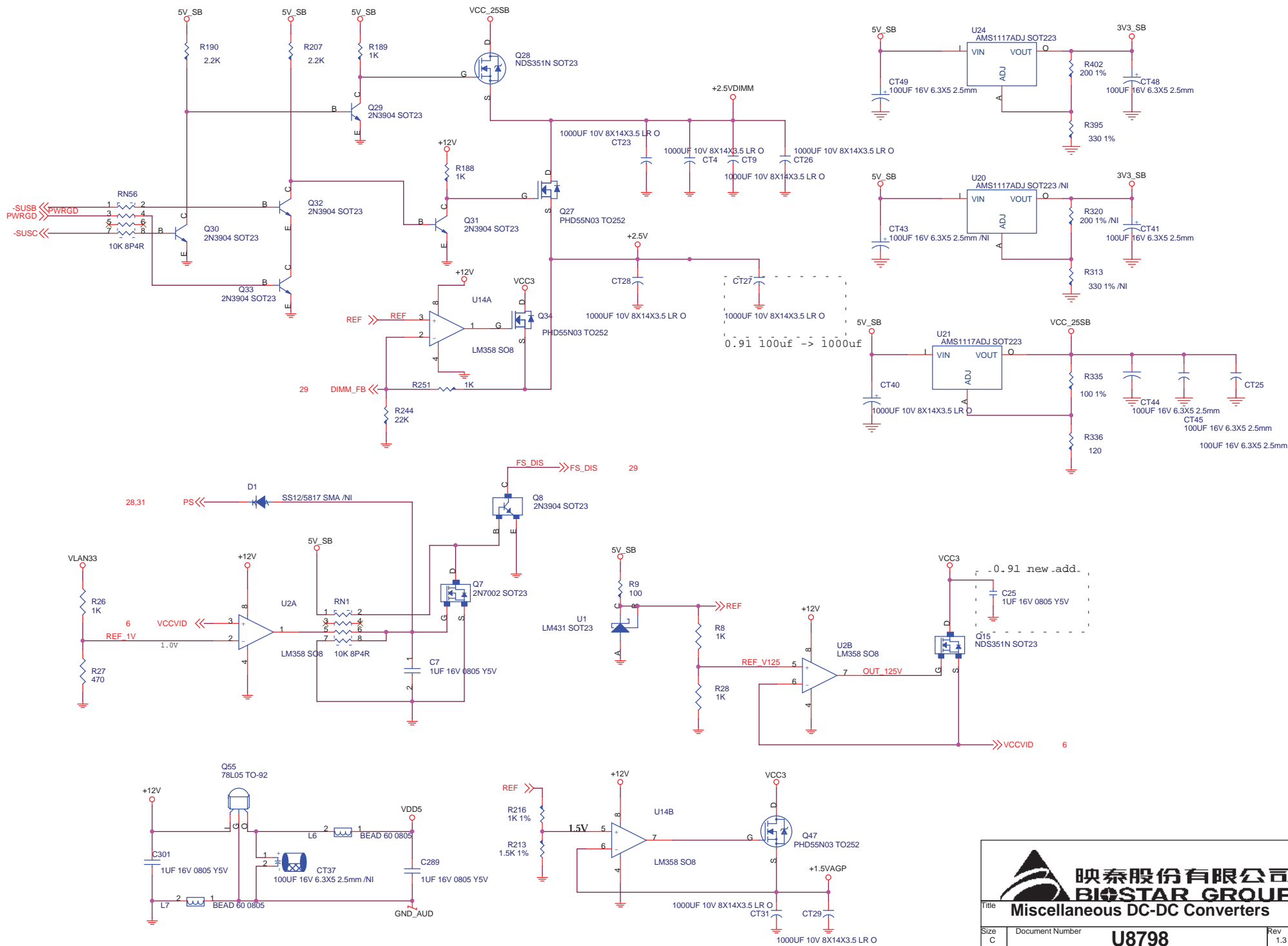


2	SLP		PWR_LED	ON/OFF		IrDA	2
1	SPEAKER		HD_LED	RESET			2

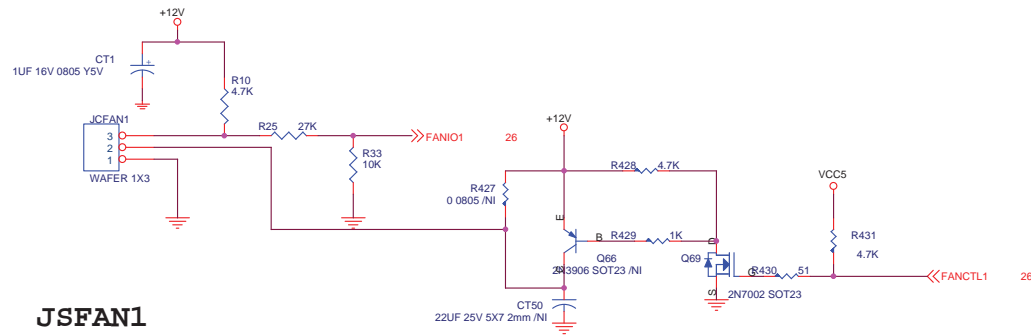




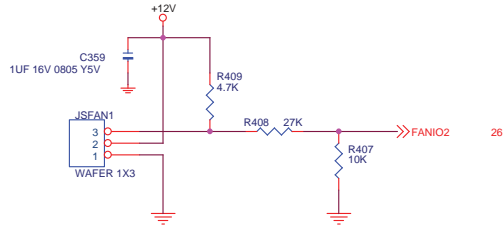




## JCFAN1

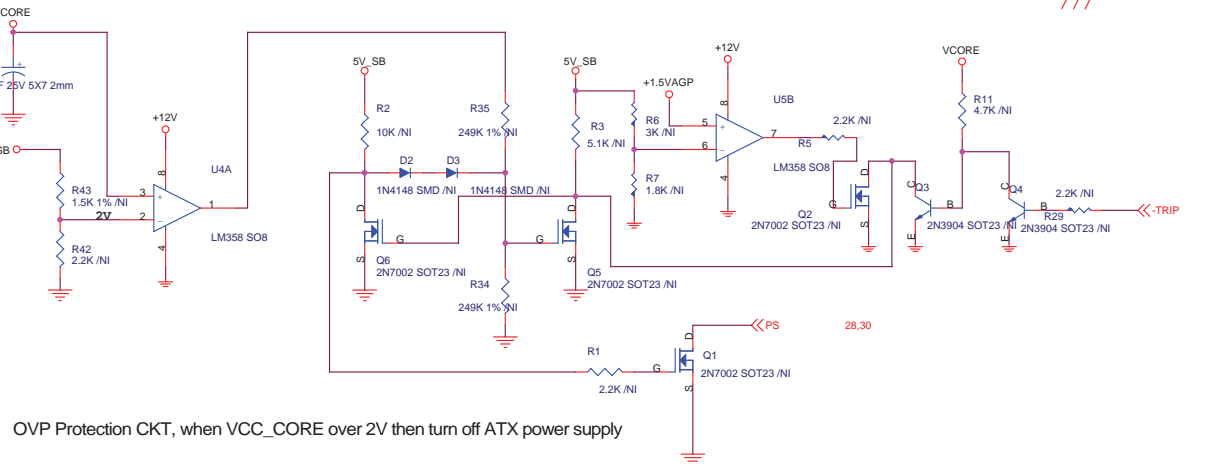
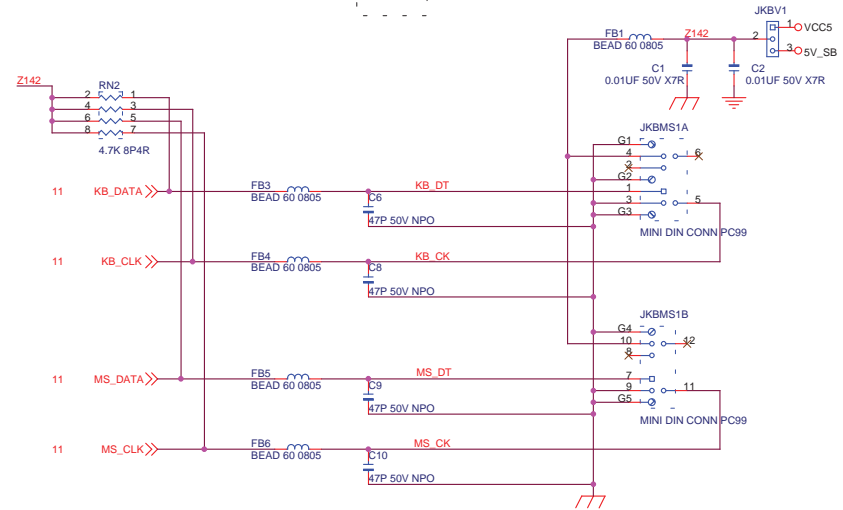


## JSFAN1



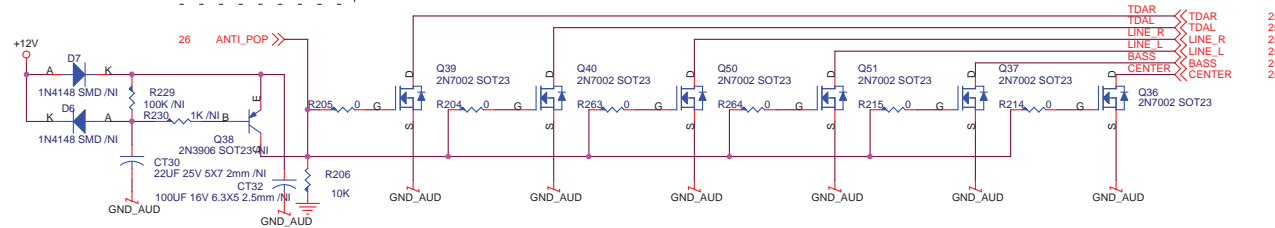
## PS2

DEFAULT : 1-2 CLOSE



OVP Protection CKT, when VCC\_CORE over 2V then turn off ATX power supply

## Anti-POP Shot



# Connect to MII interface

